



# Skills Info Sessions

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# Hubs of Excellence

DIGITAL-JU-CHIPS-2026-SKILLS-HoE-SG



€20M

Total EU budget



€4M

Estimated per project



50%

EU funding rate

Co-funded with the NFAs.



OPENS

7 July 2026



CLOSES

24 Sept 2026, 17:00

Brussels time

## ELIGIBILITY ASPECTS



**Participation restrictions** for the protection of European digital infrastructures, communication and information systems, and related supply chains.



**Ownership control:** legal entities that are established in EU Member States, EEA countries (Iceland, Liechtenstein, and Norway), but are controlled from third countries may only participate on the condition that they guarantee the protection of the essential security interests of the Union and the Member States and that they ensure the protection of classified documents information.

# Hubs of Excellence



Educational programs which aim to increase quality and quantity of highly specialized students by shortening the time to equip graduated for the work role.

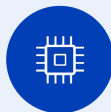


**5+ years**

minimum duration

Shall focus on only one of the following areas deemed crucial for European industry:

a)



Chip design and tools for chip design

b)



Chip manufacturing, equipment metrology and testing

c)



Chiplets, heterogeneous integration, advanced packaging

d)



Technologies for photonic components and systems

e)



Power electronics and wide bandgap materials

# Hubs of Excellence



## Outcomes

- Long-term hubs for education that will increase the number and enhance the knowledge of specialists in semiconductor technology area.
- Increase the inflow of students in these interest areas.
- Increase the mobility of students and specialists.
- Not duplicate existing initiatives but complement them, build upon existing workforce intelligence.



## Scope

- Creation of a hub specialised in one of the interest areas.
- Design of educational programs, aligning academic programmes with industry.
- Access to modern infrastructure/equipment for the specialised students.
- Intra-EU mobility, cross-border and inter-regional collaboration; suggestion to involve organizations from countries with talent surplus and talent shortages.
- Attracting talent from outside EU.
- Raising awareness, show the diversity of careers, establish career-orientation platform in micro-electronics.
- Clear KPIs.

# Pilot Federation

DIGITAL-JU-CHIPS-2026-SKILLS-PF-SG



€10M

Total EU budget



€10M

Per proposal



50%

EU funding rate



5+ yrs

Min. duration

Co-funded with the NFAs.



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# Pilot Federation



## Outcomes

- A federation of Vocational Education and Training providers in semiconductor related technology areas.
- Reskilling programmes for professionals transitioning from other industrial sectors experiencing surplus workforce.
- Increased inflow of workers, increased intra and extra EU mobility.
- Standardized curricula for technicians and recognized certifications across the EU.



## Scope

- Manage and ease mutual recognition of international systems for training, qualification and certification for technicians in the semiconductor sector.
- Provide a comprehensive overview of the European providers and of their micro-credentials offer.
- Industry-aligned micro-credentials.
- Intra-EU mobility, cross-border and inter-regional collaboration.
- Attracting talent from outside EU.
- Clear KPIs.

# Stimulation of Chip Design

DIGITAL-JU-CHIPS-2026-SKILLS-SCD-CSA



€15M

EU budget



100%

EU funded



60%

to student tape-outs



4 yrs

Project duration



OPENS

7 July 2026



CLOSES

24 Sept 2026, 17:00

Brussels time

## ELIGIBILITY ASPECTS



**Participation restrictions** for the protection of European digital infrastructures, communication and information systems, and related supply chains.

# Stimulation of Chip Design

- The topic might be amended, including:
  - Some changes in scope/expected outcomes;
  - Maximum consortium size: 12 (indicative value, may be exceeded with a proper justification);
  - Ownership control restrictions (like in the other two topics).
- <https://www.chips-ju.europa.eu/>
  - Check the soon to be updated Work Programme (Appendix 8)